

Our Case No.: 11474-4

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

John Nicholas COLEMAN

Serial No.: To be assigned

Filing Date: March 22, 2004

For: LOGARITHMIC ARITHMETIC
SYSTEM

Examiner: To be assigned

Group Art Unit No.: To be assigned

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56, it is respectfully requested that this Information Disclosure Statement be entered and the documents listed on the attached Form PTO-1449 be considered by the Examiner and made of record. Copies of the listed documents required by 37 C.F.R. § 1.98(a)(2) are enclosed for the convenience of the Examiner.


In accordance with 37 C.F.R. § 1.97(g),(h), this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed to be an admission that the information cited is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

This Information Disclosure Statement is being filed prior to the receipt of the first Official Action reflecting an examination on the merits and hence is believed to be timely filed in accordance with 37 C.F.R. § 1.97(b). Therefore, no fees are believed to be due in connection with filing of this Information Disclosure Statement.

Applicant(s) respectfully request that the listed documents be made of record in the present case.

Respectfully submitted,

22 March 2004
Date


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| FORM PTO-1449 | SERIAL NO. | CASE NO. 11474-4 |
| LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT | FILING DATE March 22, 2004 | GROUP ART UNIT 2121 |
| (use several sheets if necessary) | APPLICANT(S): John Nicholas COLEMAN | |

REFERENCE DESIGNATION
U.S. PATENT DOCUMENTS

| EXAMINER INITIAL | DOCUMENT NUMBER | DATE | NAME | CLASS/ SUBCLASS | FILING DATE |
|---------------------|--------------------|------|------|--------------------|----------------|
| | N/A | | | | |

FOREIGN PATENT DOCUMENTS

| EXAMINER INITIAL | DOCUMENT NUMBER | DATE | COUNTRY | CLASS/ SUBCLASS | TRANSLATION YES NO |
|---------------------|--------------------|----------------|-----------|----------------------|-----------------------|
| | A1 | PCT/GB99/01359 | 8/20/1999 | PCT Search Report | |

| EXAMINER INITIAL | OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.) | |
|---------------------|--|--|
| | A2 | Coleman, J.N., "Simplification of table structure in logarithmic arithmetic," <i>ELECTRONICS LETTERS</i> , 1995, Vol. 31, No. 22, pp. 1905-1906. |
| | A3 | Ohhashi, M. et al., "High-speed computation of unary functions," <i>Proceedings of 7th Symposium on Computer Arithmetic</i> (Cat. No. 85CH2146-9), 1985, pp. 82-85. |

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|----------|-----------------|
| EXAMINER | DATE CONSIDERED |
|----------|-----------------|

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.